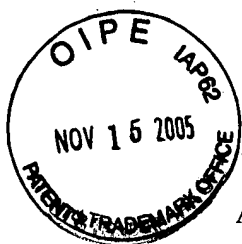


Ifw



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Claude R. Gauthier, Pradeep R. Trivedi, Raymond A. Heald, Gin S. Yee
Assignee: Sun Microsystems, Inc.
Title: Compensation Technique to Mitigate Aging Effects in Integrated Circuit Components
Serial No.: 10/771,989 Filed: February 4, 2004
Examiner: Arnold M. Kinkead Group Art Unit: 2817
Docket No.: P9427 Customer No.: 33438

Austin, Texas
November 10, 2005

MAIL STOP AMENDMENT
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

RESPONSE TO NON-FINAL OFFICE ACTION

Sir:

This paper is responsive to the Office Action dated August 10, 2005, having a shortened statutory period expiring on November 10, 2005. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.

11/16/2005 LWONDIM1 00000020 10771989

01 FC:1201

200.00 OP